

FORM 1449* INFORMATION DISCLOSURE STATEMENT IN AN APPLICATION (Use several sheets if necessary)	Docket Number: 10873.1298USW1	Application Number: 10/695,731
	Applicant: HIRAMOTO et al.	
	Filing Date: October 24, 2003	Group Art Unit: <u>Unknown</u> <u>2824</u>



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TL	6,169,689	01.2001	NAJI	—	—	
TL	6,335,236	01.2002	MORI	—	—	
TL	2002/0149962	10.2002	HORIGUHI	—	—	
TL	5,930,164	07.1999	ZHU	—	—	
TL	2001/0026470	10.2001	GILLIES	—	—	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
TL	11-354728	12.1999	Japan	—	—	Partial & Abstract	
TL	8-306014	11.1996	Japan	—	—	Abstract	
TL	2001-338487	12.2001	Japan	—	—	Abstract	
TL	495745	03.1991	TWP	—	—	See IDS	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

TL		Roy Scheuerlein et al., "ISSCC 2000 SESSION 7 TD: EMERGING MEMORY & DEVICE TECHNOLOGIES PAPER TA 7.2", in 2000 IEEE International Solid-State Circuits Conference, DIGEST OF TECHNICAL PAPERS, pp 128-129
TL		M. Durlam et al., "ISSCC 2000 SESSION 7 TD: EMERGING MEMORY & DEVICE TECHNOLOGIES PAPER TA 7.3", in 2000 IEEE International Solid-State Circuits Conference, 2000 IEEE, DIGEST OF TECHNICAL PAPERS, PP 130-131

EXAMINER	TDAN LG	DATE CONSIDERED	4/30/2005
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.			